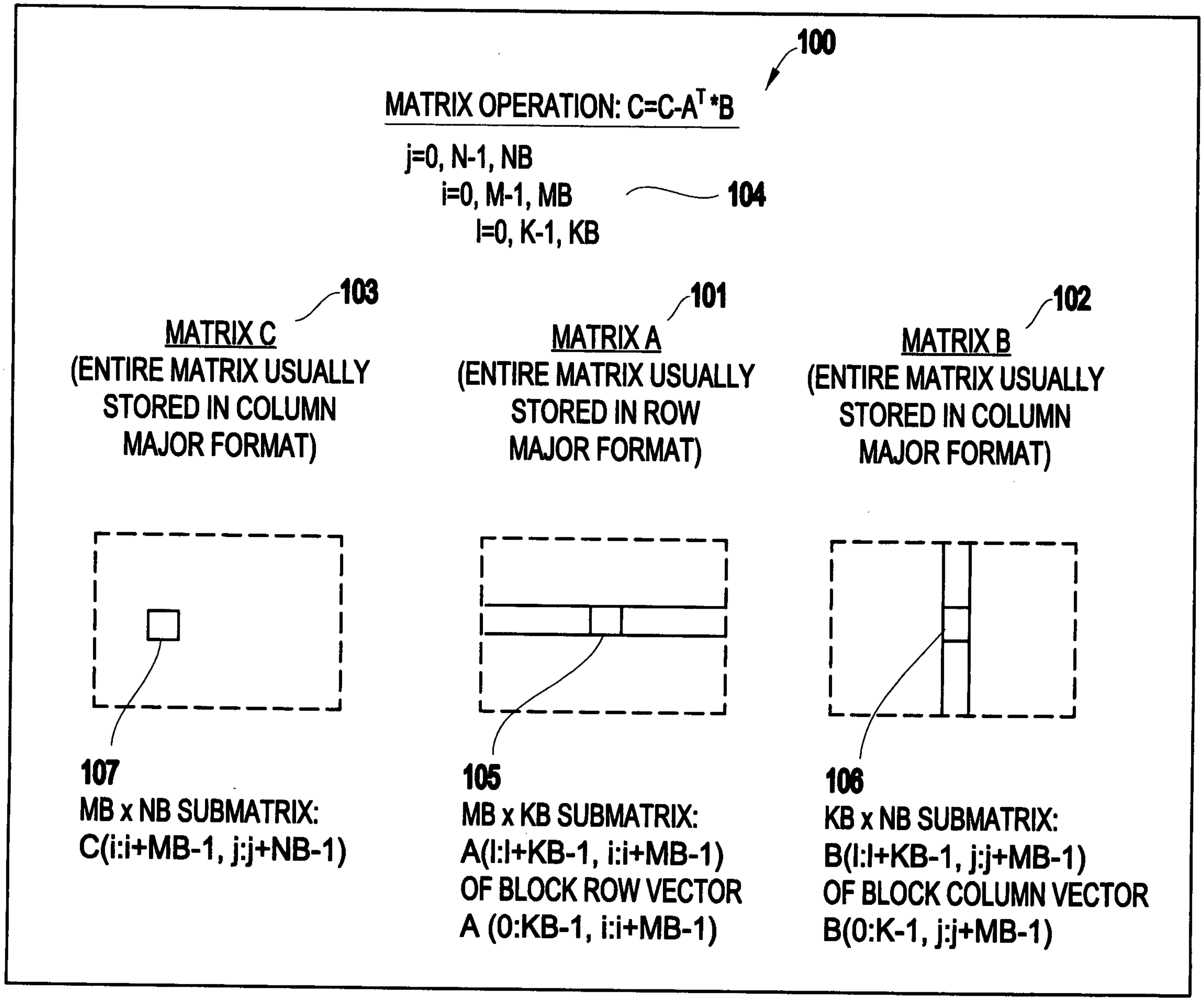
100
MATRIX OPERATION: C=C-A^T*B
j=0, N-1, NB
i=0, M-1, MB
l=0, K-1, KB
104
103
MATRIX C
(ENTIRE MATRIX USUALLY STORED IN COLUMN MAJOR FORMAT)
101
MATRIX A
(ENTIRE MATRIX USUALLY STORED IN ROW MAJOR FORMAT)
102
MATRIX B
(ENTIRE MATRIX USUALLY STORED IN COLUMN MAJOR FORMAT)
107
MB x NB SUBMATRIX:
C(i:i+MB-1, j:j+NB-1)
105
MB x KB SUBMATRIX:
A(l:l+KB-1, i:i+MB-1)
OF BLOCK ROW VECTOR
A (0:KB-1, i:i+MB-1)
106
KB x NB SUBMATRIX:
B(l:l+KB-1, j:j+MB-1)
OF BLOCK COLUMN VECTOR
B(0:K-1, j:j+MB-1)

FIG.1

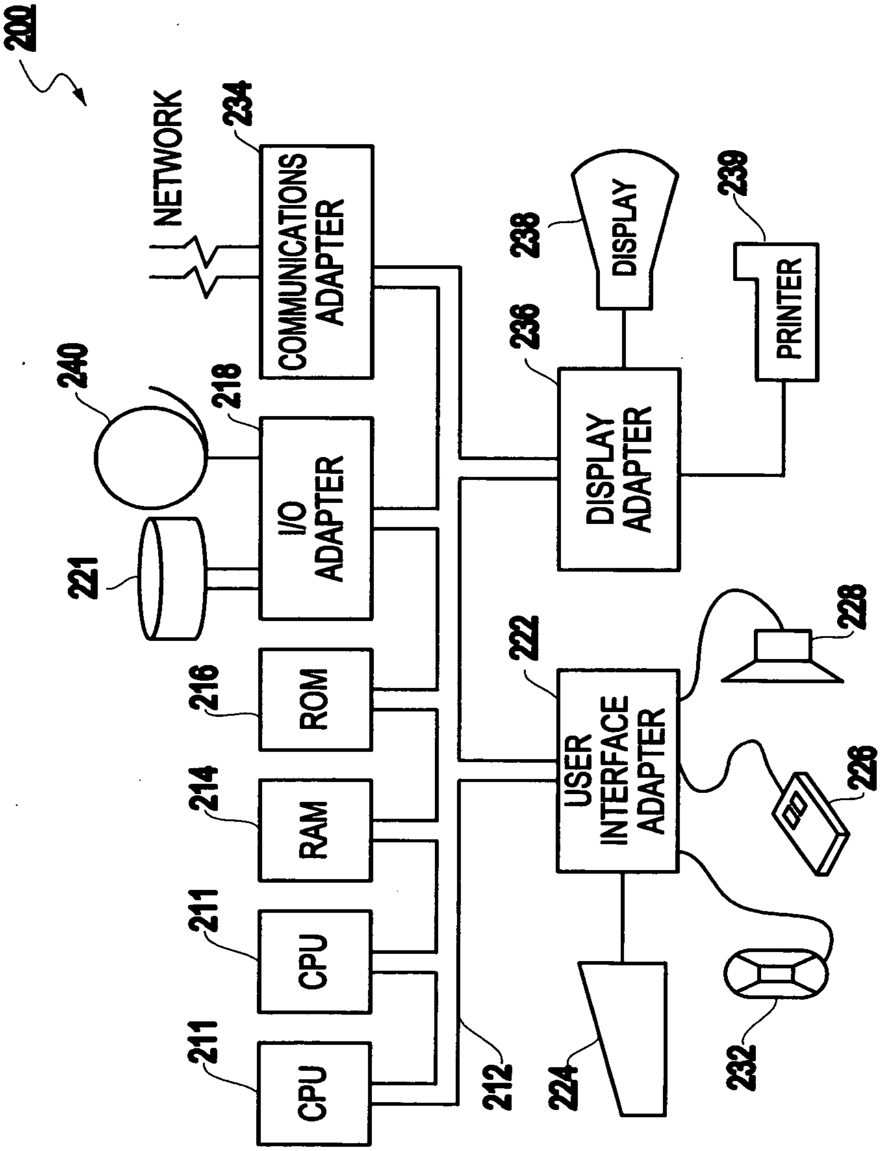
200
CPU
211
CPU
211
RAM
214
ROM
216
I/O ADAPTER
218
221
240
COMMUNICATIONS ADAPTER
234
NETWORK
212
USER INTERFACE ADAPTER
222
224
232
226
228
DISPLAY ADAPTER
236
DISPLAY
238
PRINTER
239

FIG.2

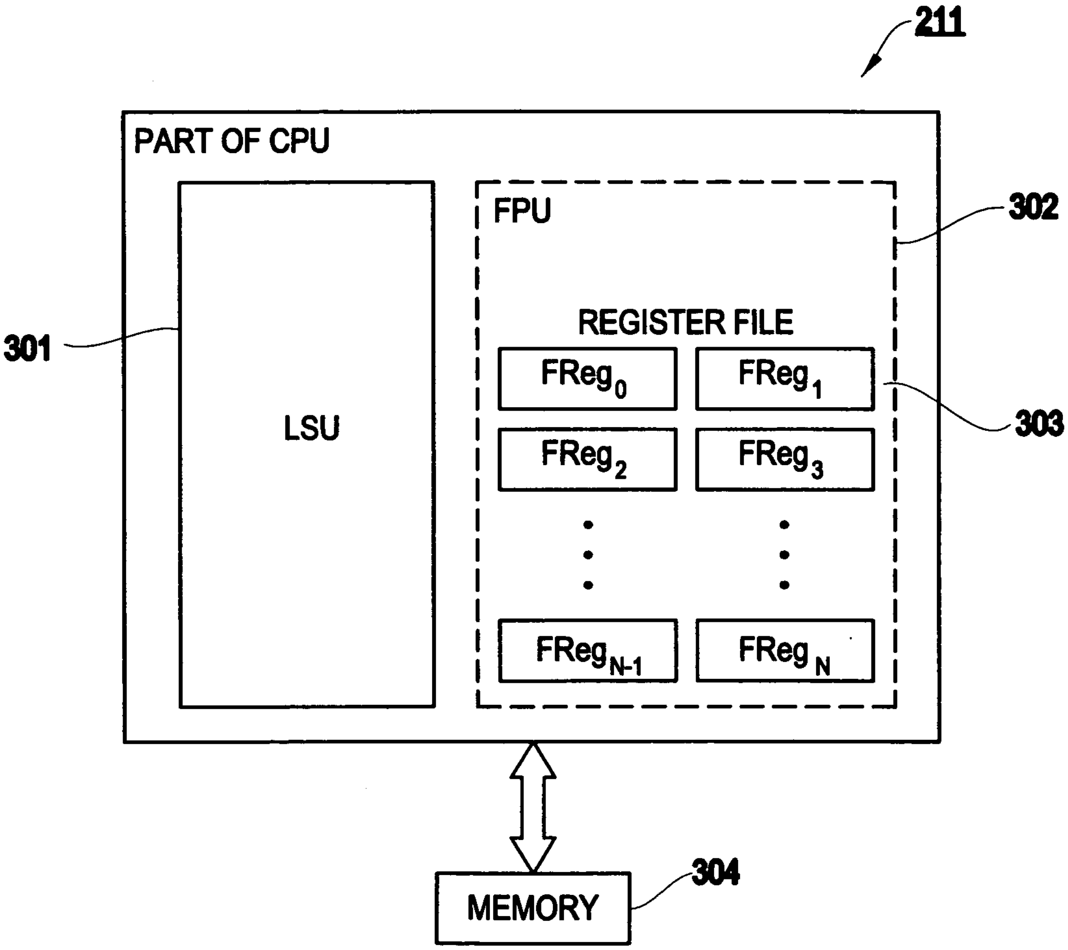
211
PART OF CPU
301
LSU
FPU
302
REGISTER FILE
$FReg_0$
$FReg_1$
$FReg_2$
$FReg_3$
$FReg_{N-1}$
$FReg_N$
303
MEMORY
304

FIG.3

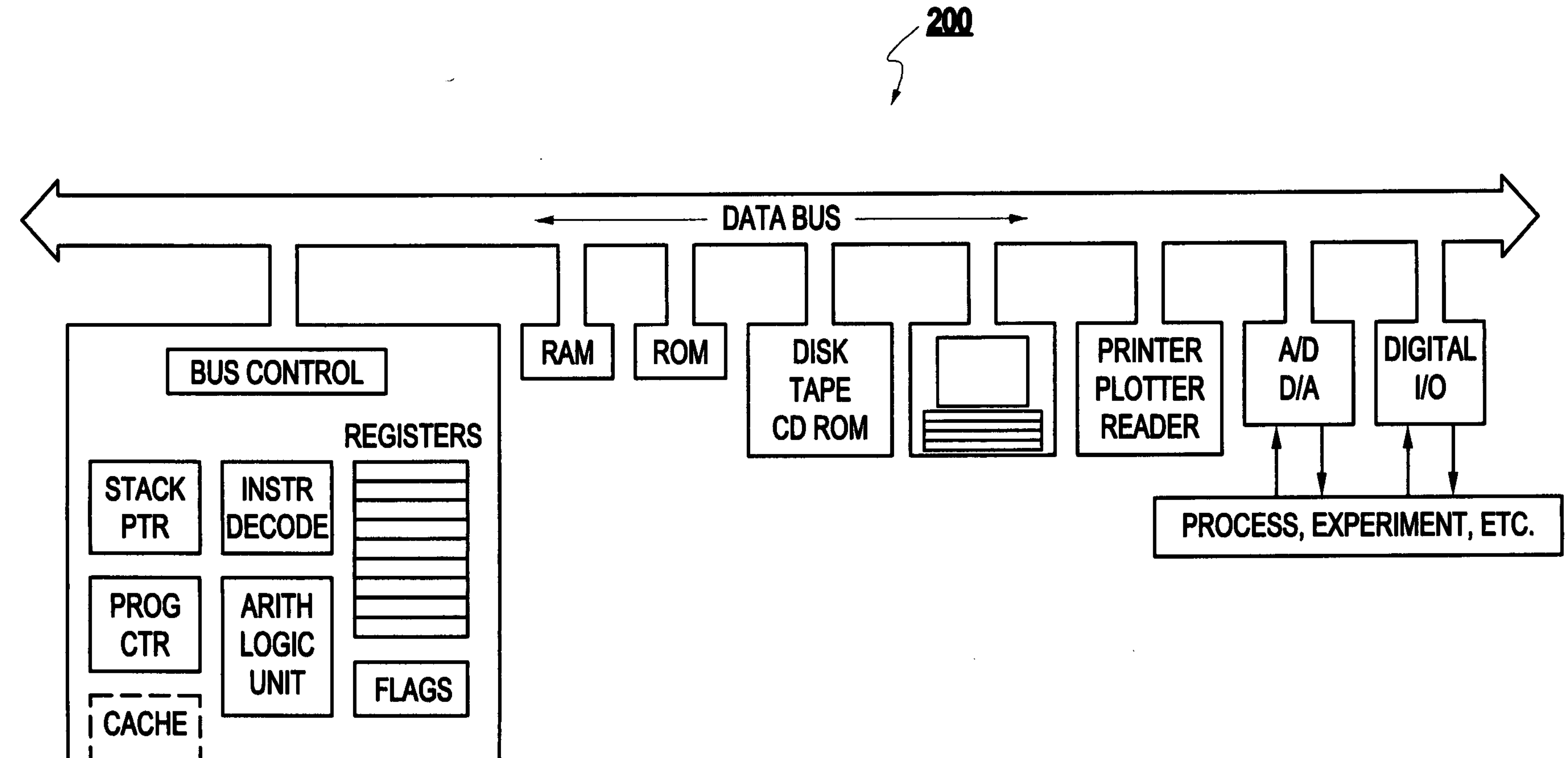
200
DATA BUS
BUS CONTROL
REGISTERS
STACK PTR
INSTR DECODE
PROG CTR
ARITH LOGIC UNIT
FLAGS
CACHE
CPU
401
211
RAM
ROM
DISK TAPE CD ROM
PRINTER PLOTTER READER
A/D D/A
DIGITAL I/O
PROCESS, EXPERIMENT, ETC.

FIG.4

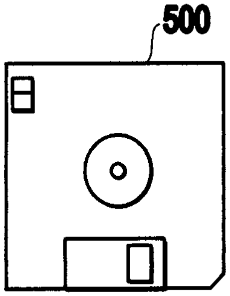
500

FIG.5